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JUL 14 2004

Customer No.: 31561  
Application No.: 10/064,454  
Docket No.: 8327-US-PA

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICIAL

In Re Application of: )  
KUO et al. ) Examiner : John M Ross  
Serial No. : 10/064,454 ) Art Unit : 2188  
Filed : 07/17/2002 ) Docket No. : 8327-US-PA  
For : Control Chip and Method For )  
Accelerating Memory Access )

*No fee is believed to be due. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-2620 (Order No.: 8327-US-PA)*

AMENDMENT AND RESPONSE AFTER FINAL OFFICE ACTION

Do not enter.  
8/15/04  
U.S. Patent and Trademark Office  
Commissioner for Patents  
2011 South Clark Place  
Customer Window, Mail Stop AF  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Dear Sir:

The Office Action mailed April 15, 2004 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.

Page 1 of 14